

ABSTRACT

A simulator apparatus comprises a simulator model including a functional model for CPU constituting a system
5 to be simulated and a simulator model including a functional model for hardware to be connected to buses linked to the CPU. The respective simulator models include plural types of interfaces. The plural types of the interfaces enable plural types of simulators for different uses to access the
10 functional models. The simulator apparatus further comprises a simulator controlling device for selecting any of the plural types of the interfaces and accessing the respective functional models via the selected interfaces.